

FIG. 1

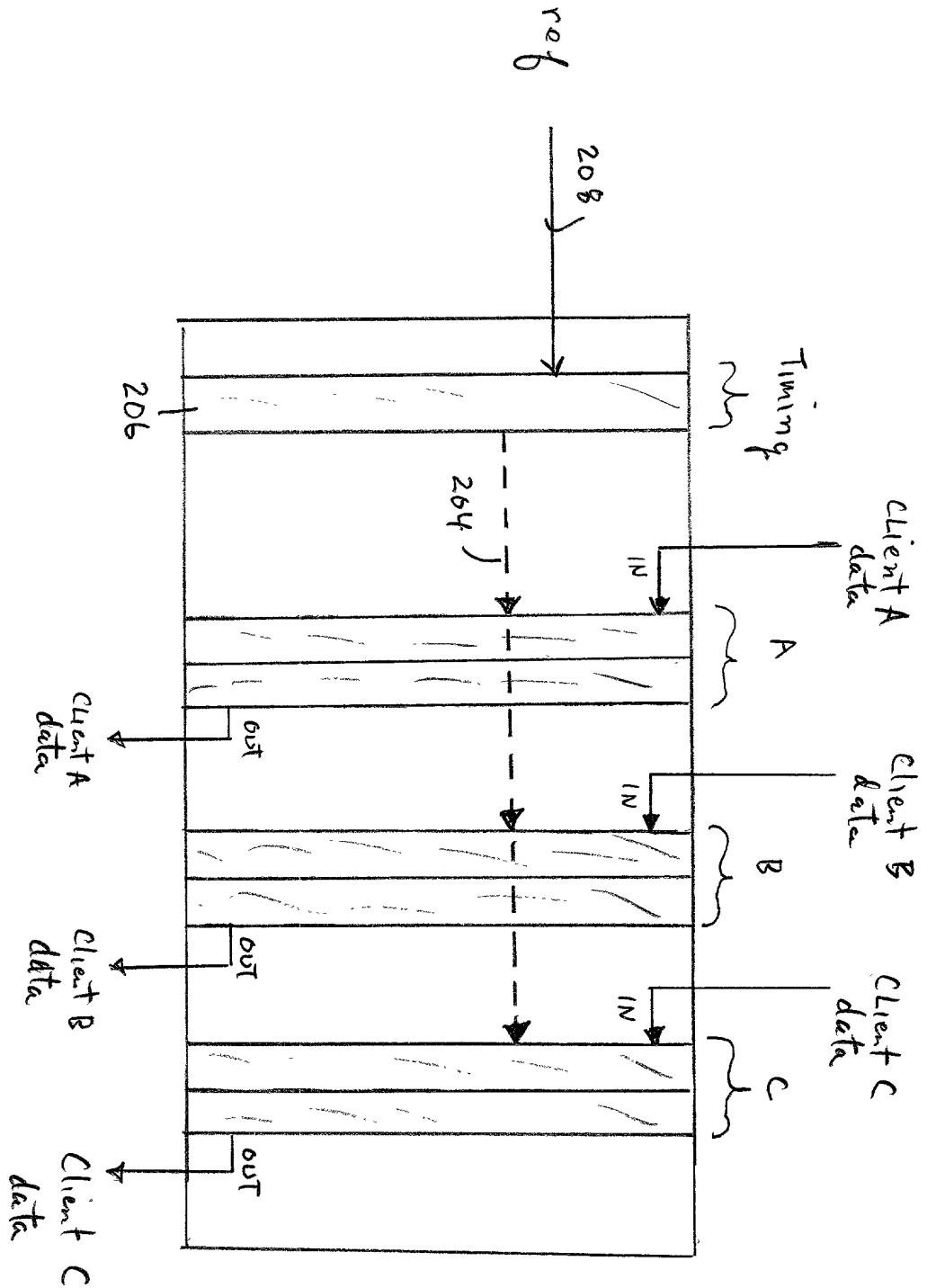


FIG. 2

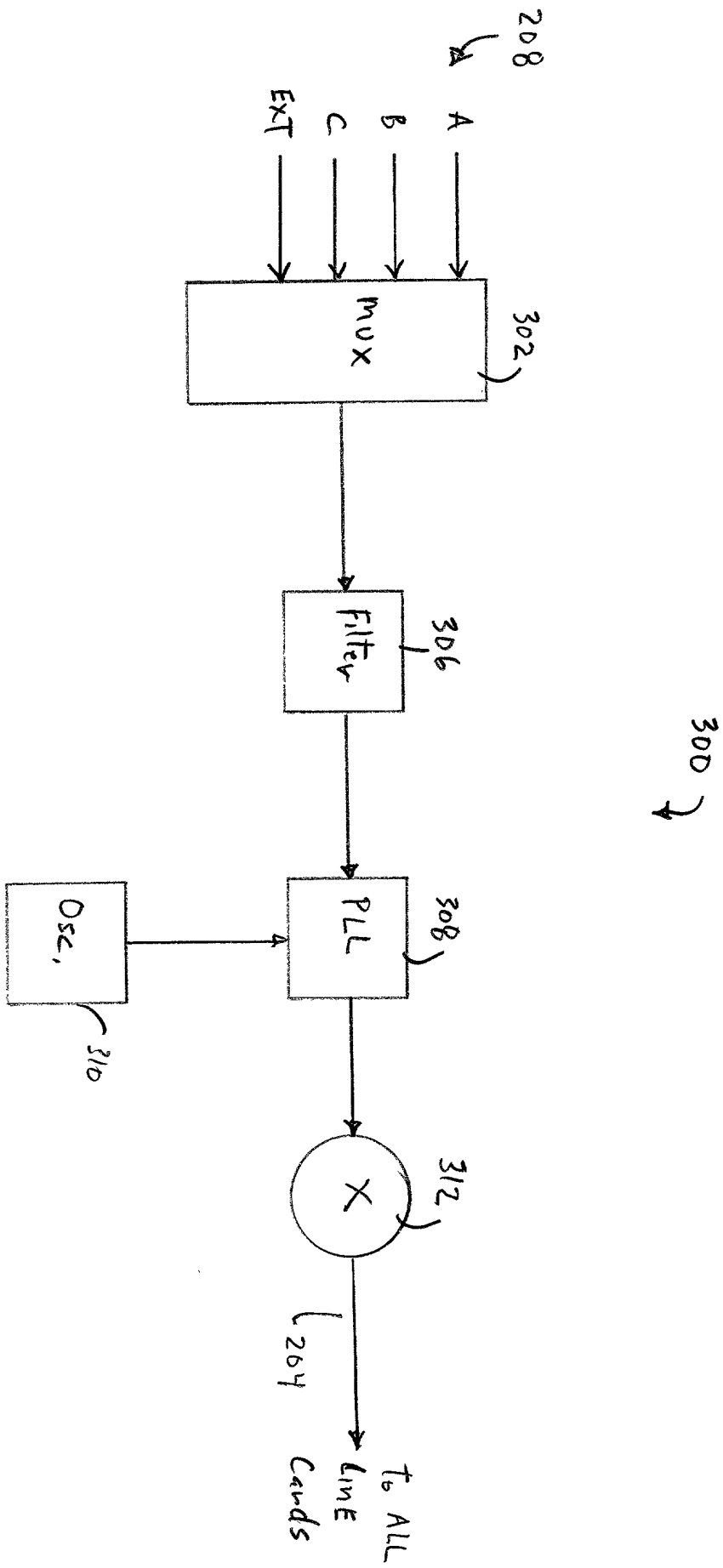


FIG. 3

FIG. 3 is a block diagram of a system 300 for processing signals. The system 300 includes a multiplexer (MUX) 302, a filter 306, a phase-locked loop (PLL) 308, an oscillator (Osc.) 310, and a multiplier (X) 312. The MUX 302 receives four inputs: A, B, C, and EXT. The output of the MUX 302 is connected to the filter 306. The output of the filter 306 is connected to the PLL 308. The PLL 308 also receives a reference signal from the oscillator 310. The output of the PLL 308 is connected to the multiplier 312. The output of the multiplier 312 is labeled 204 and is connected to a line labeled "Line" and "ALL Cards".

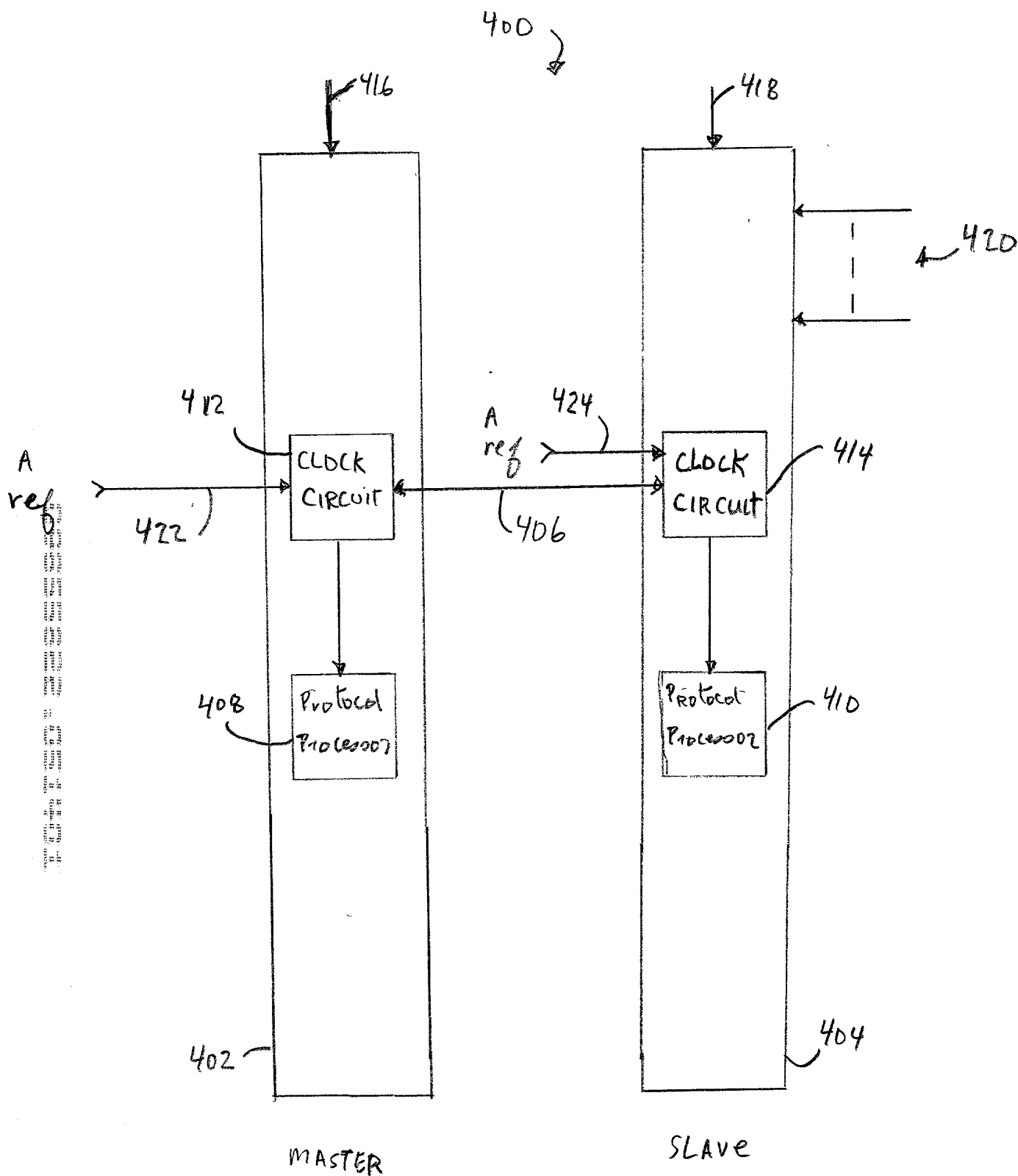


FIG. 4

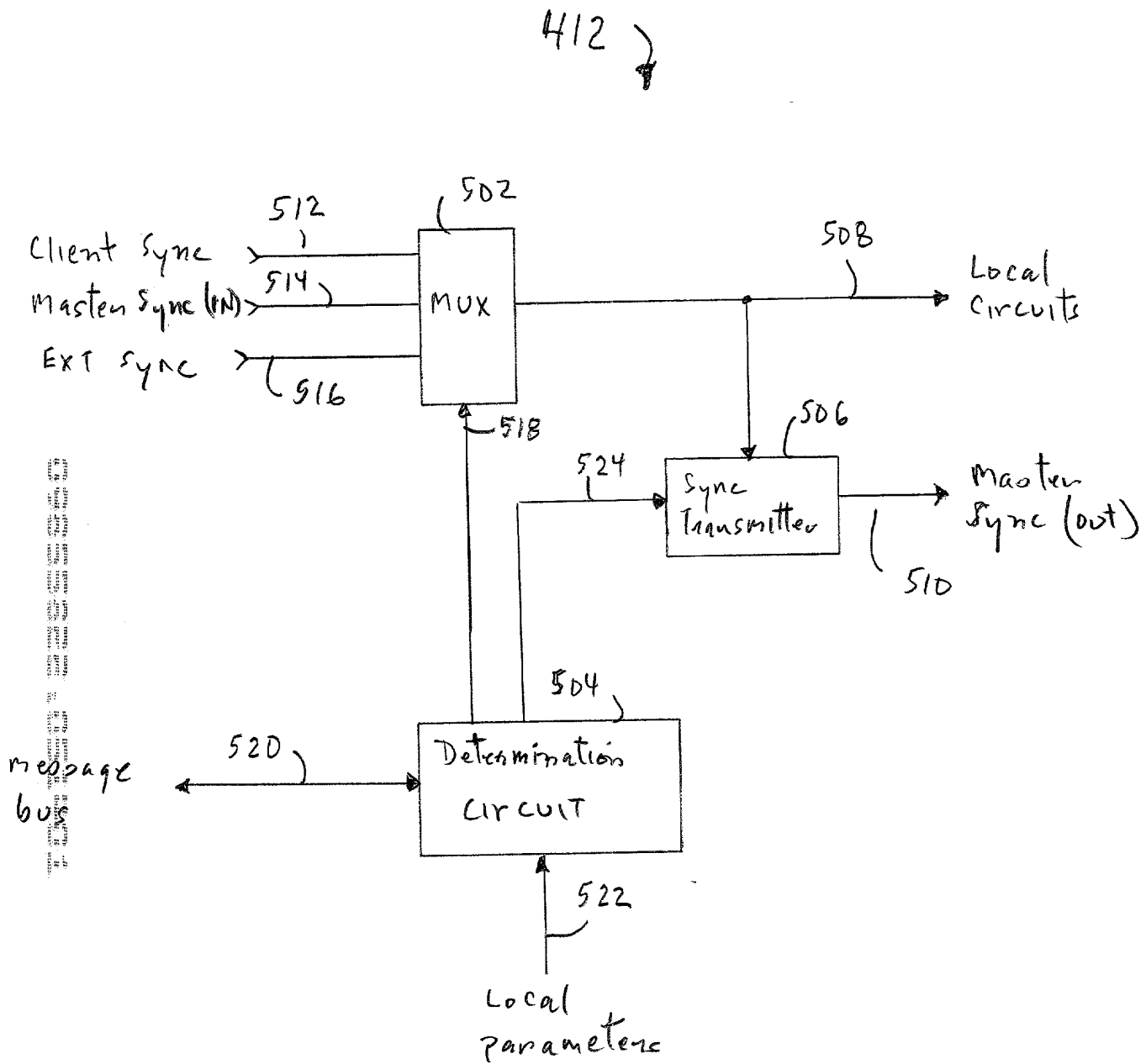


FIG. 5

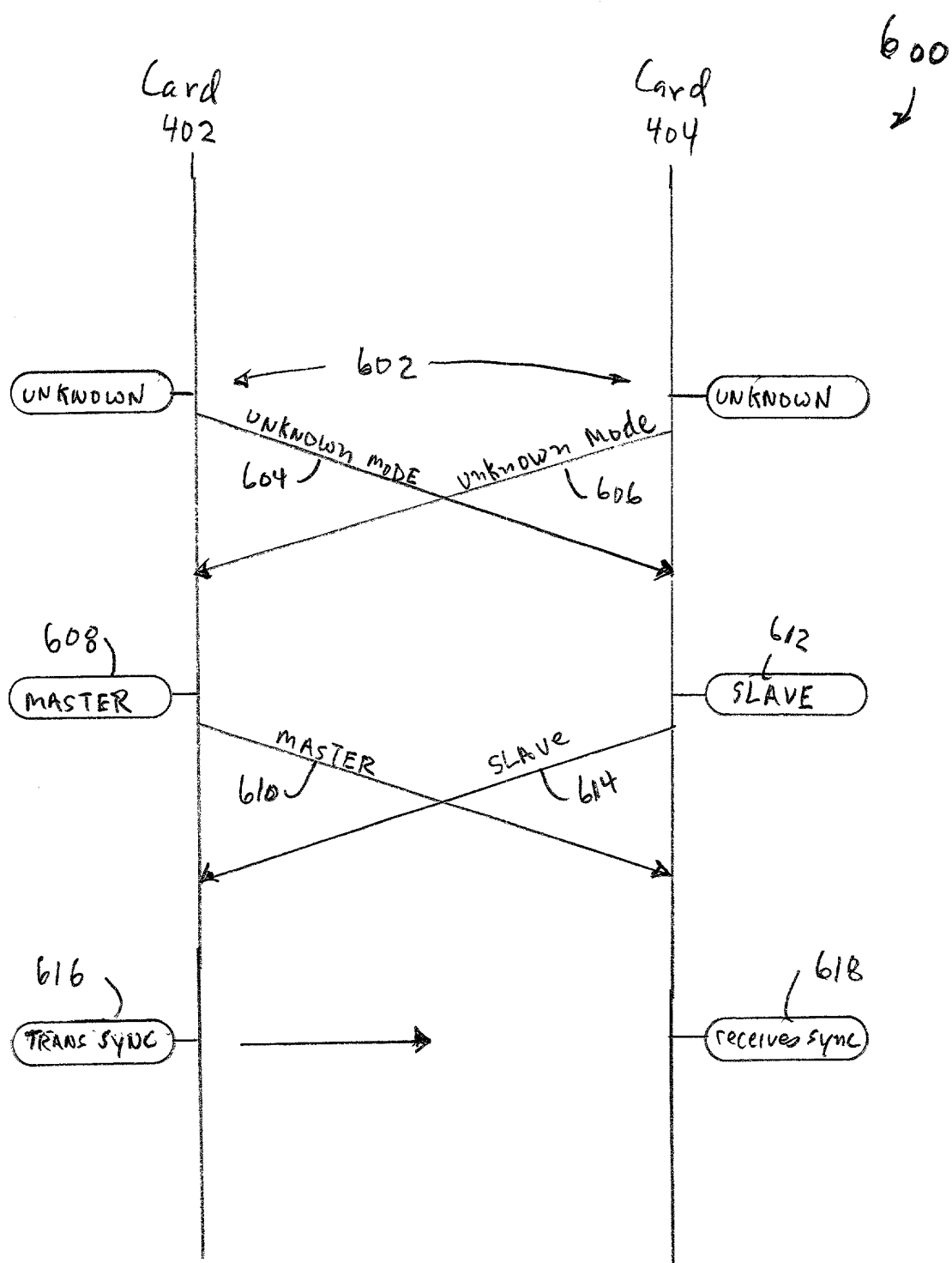


FIG. 6